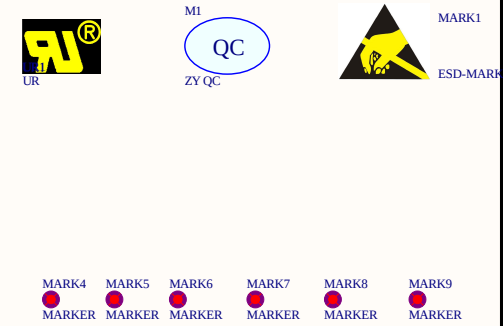
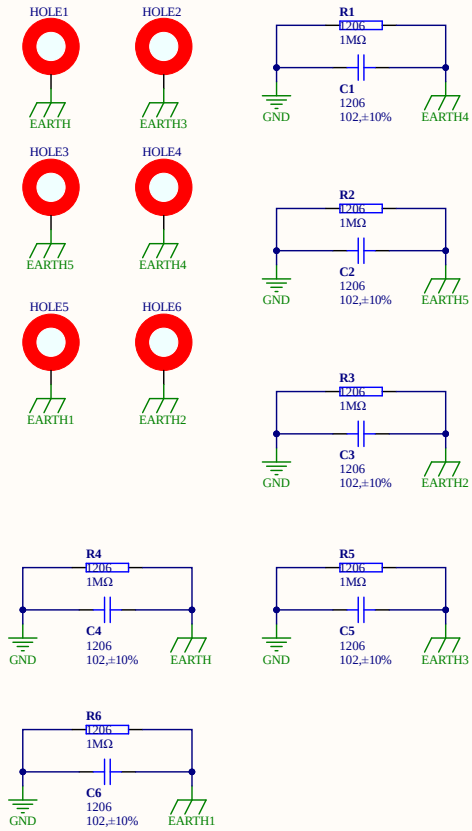
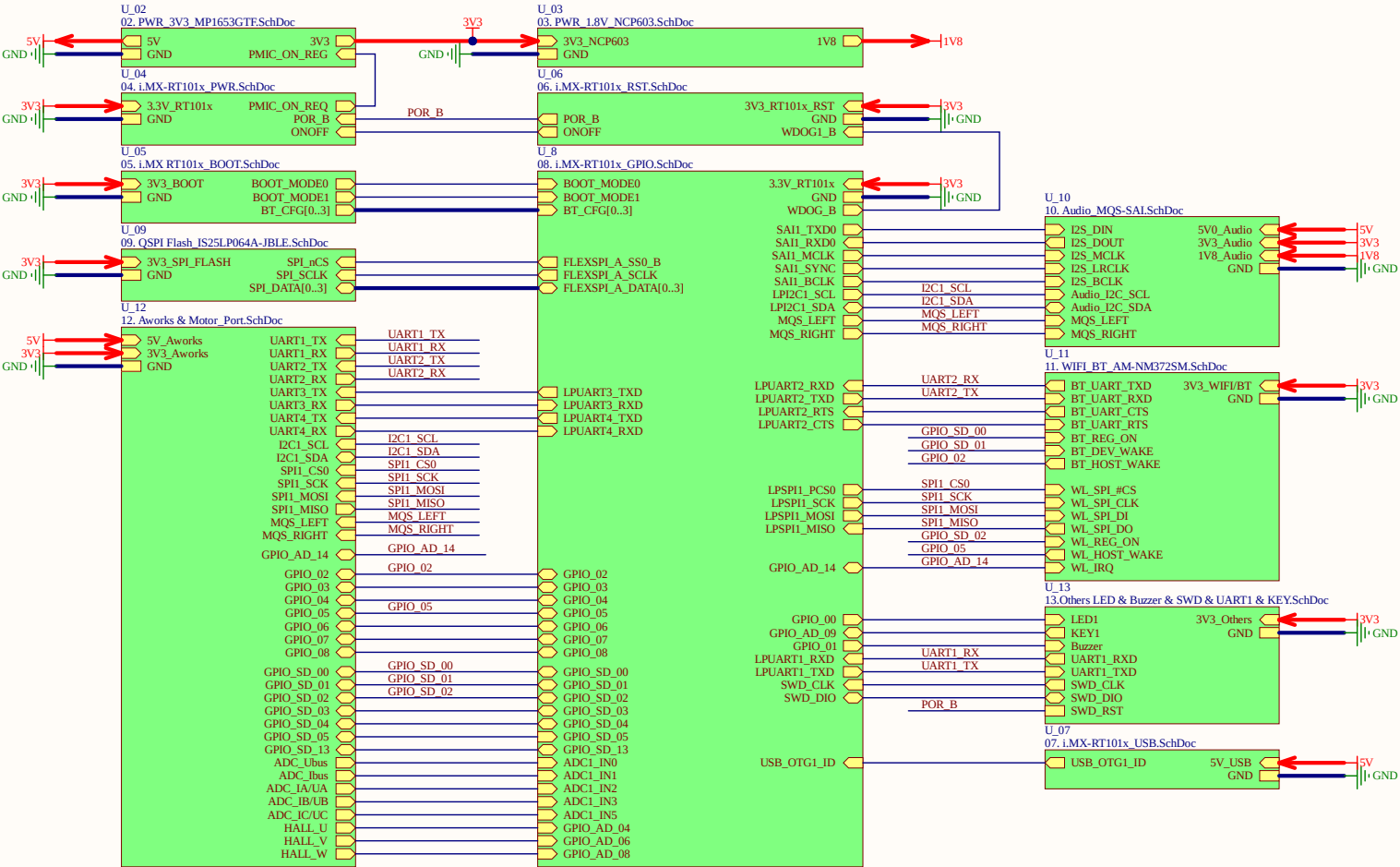
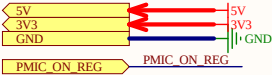


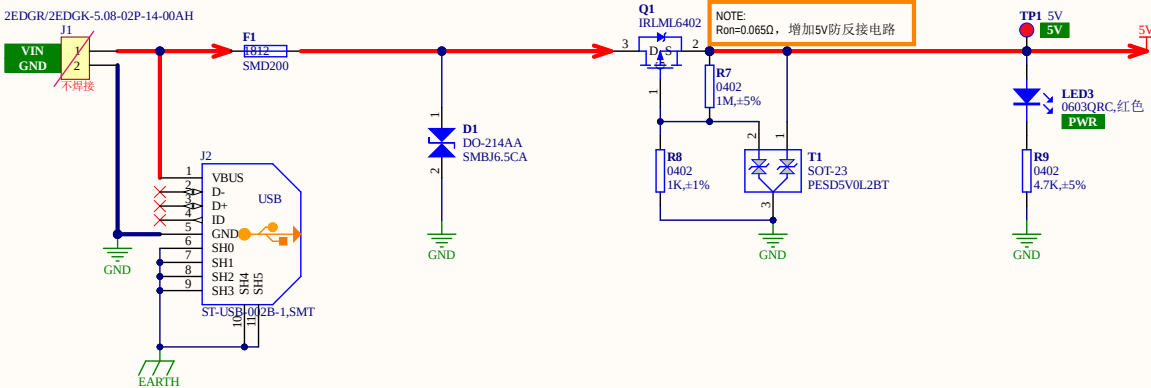
1.MAIN



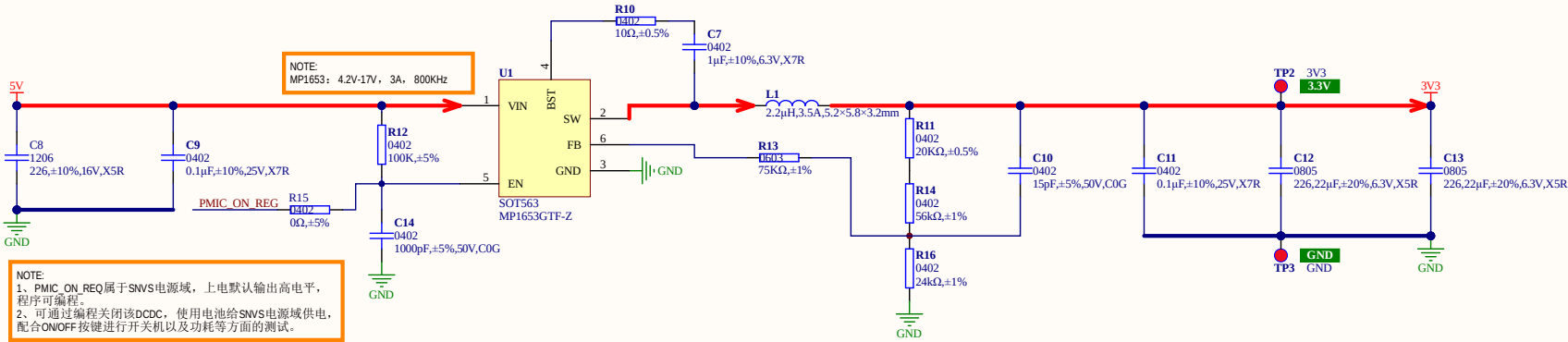
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(2-1) 5V



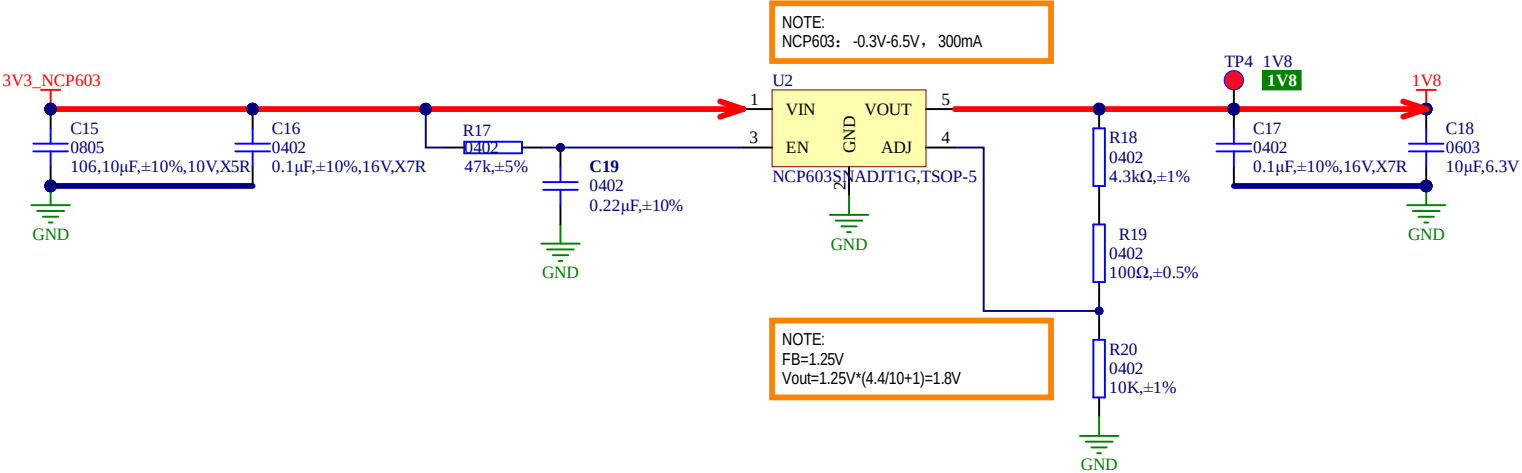
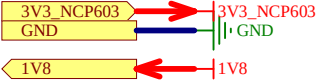
(2-2) 3V3



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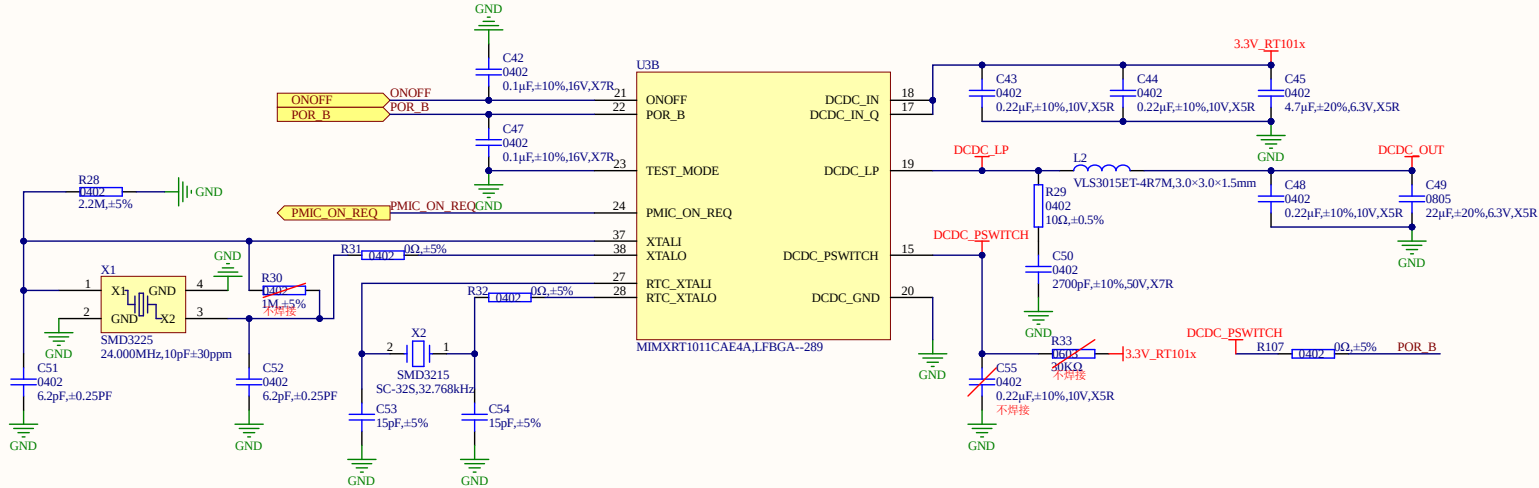
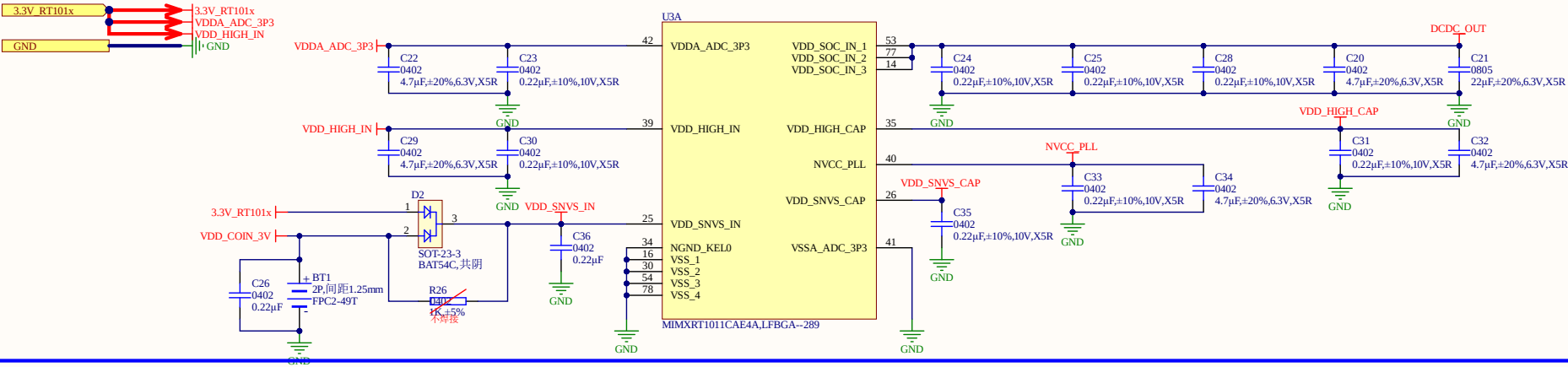
3.PWR_1.8V_NCP603

(3-1) 1V8

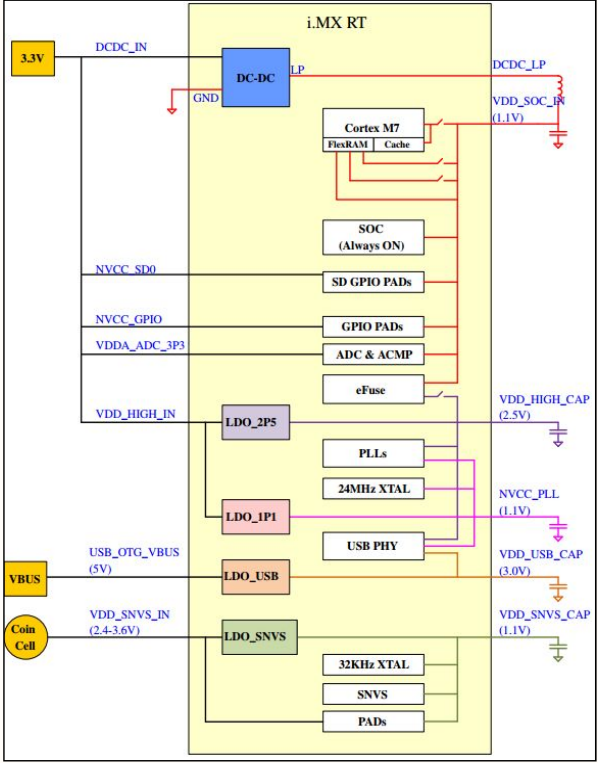
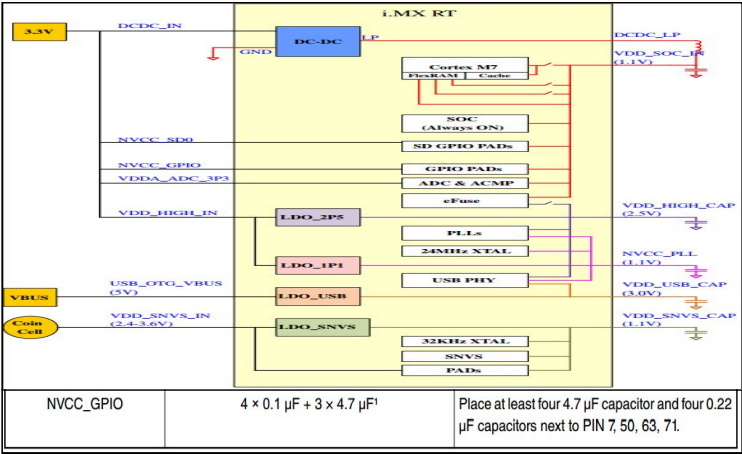


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4.i.MX-RT101x_PWR

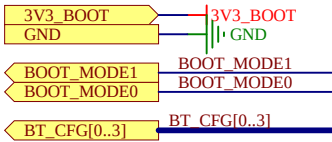


NOTE:
去耦电容的数量以及位置请严格按照此说明。

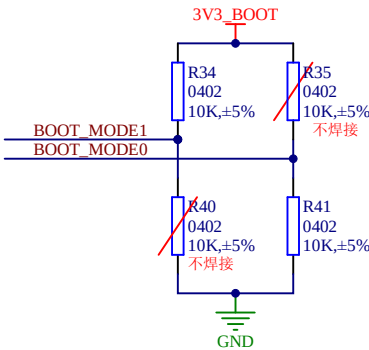


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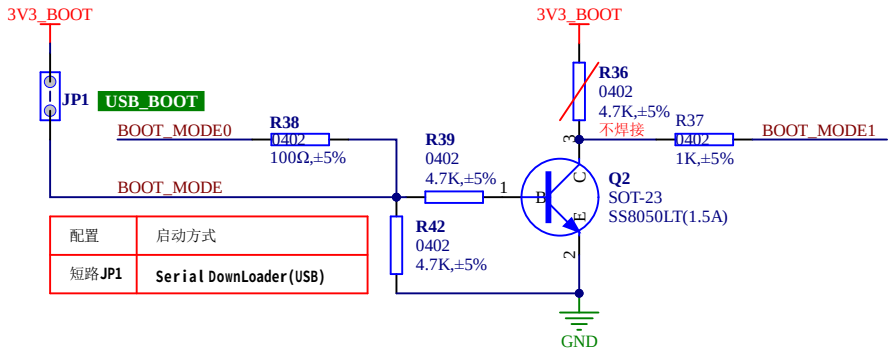
5.i.MX-RT101x_BOOT



(5-1) BOOT_MODE

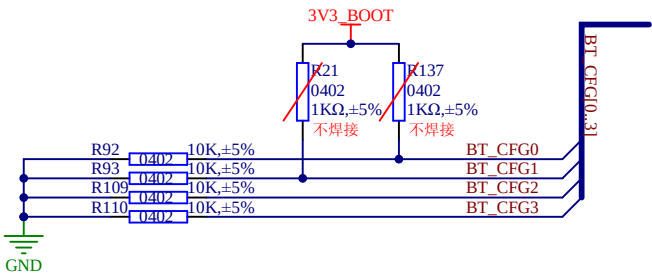


BOOT_MODE[1:0]	BOOT_TYPE
00	Boot From Fuse
01	Serial DownLoader
10	Internal Boot(Default)
11	Reserved



配置	启动方式
短路JP1	Serial DownLoader(USB)

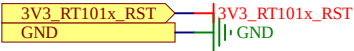
(5-2) BT_CFG



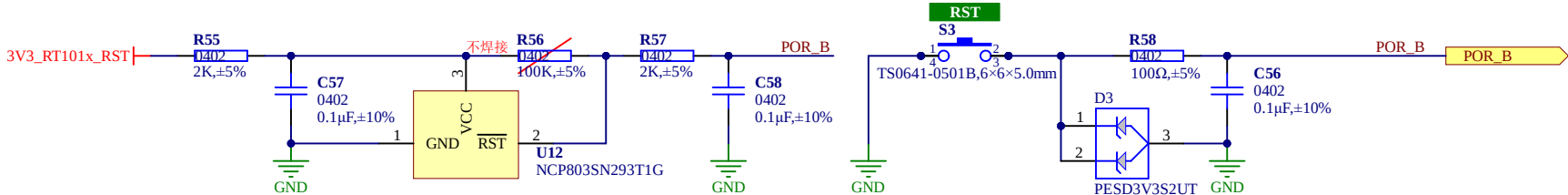
CHIP	BOOT TYPE	BOOT_CFG3	BOOT_CFG2	BOOT_CFG1	BOOT_CFG0
RT1011	FlexSPI1 Serial NOR	Reserved	FLASH_TYPE 00-Device supports 3B read by default 01-HyperFlash 3V3 10-MXIC Octal DDR 11-Micron Octal DDR		Encrypted XIP 0-Disable 1-Enable

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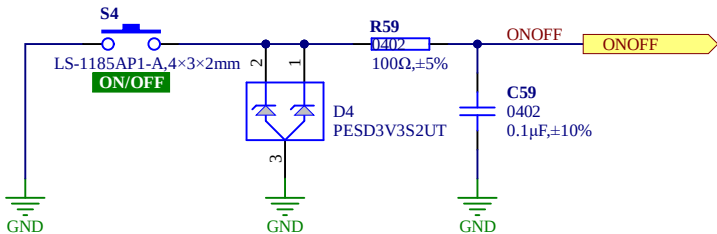
6.i.MX-RT101x_RST



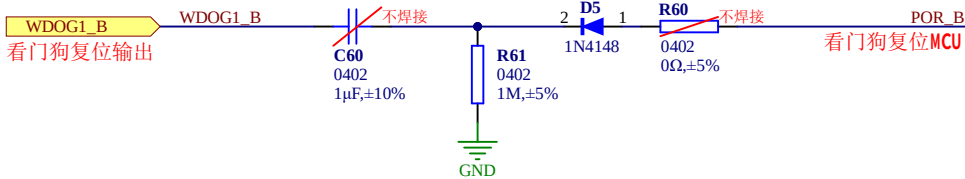
(6-1) RT101x_RST



(6-2) RT101x_FUN_Button



(6-3) SYS_WDG_RST

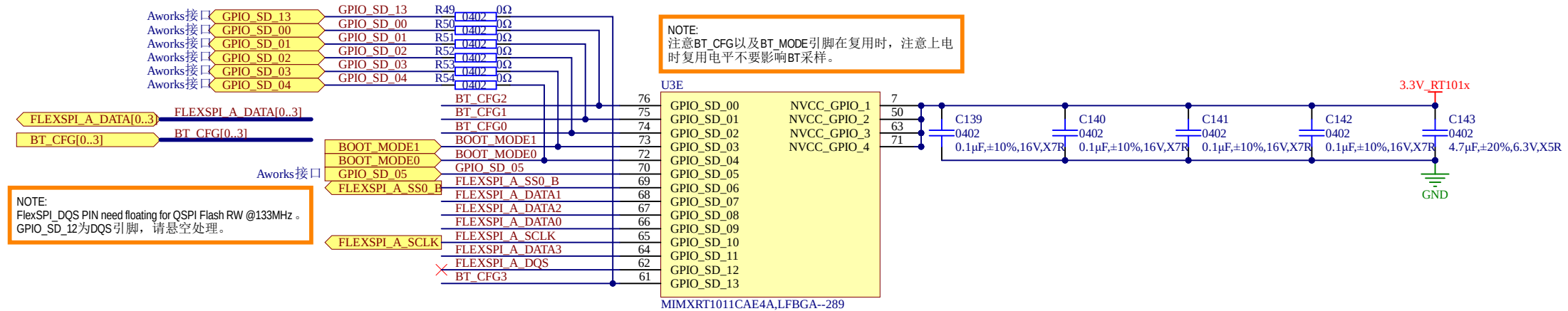
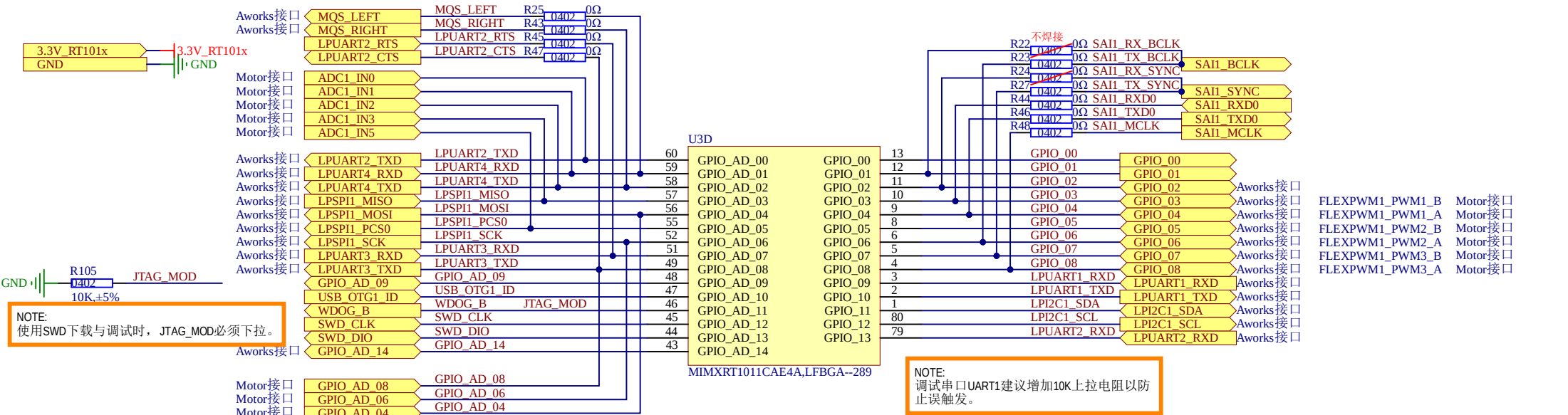


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7.i.MX-RT101x_USB

	#	修改日期	修改内容
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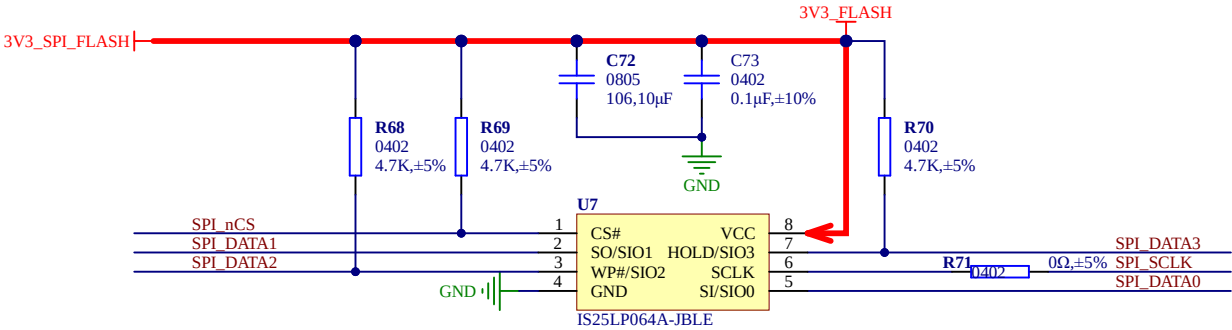
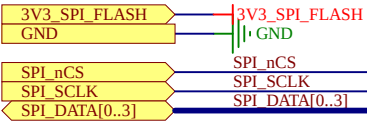
8.<图纸名称>



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9.QSPI Flash_IS25LP064ALBLE

(9-1) QSPI Flash-8MB

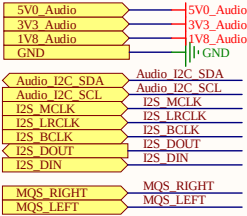


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设计人员	*	设计日期	2019.10	图纸名称	QSPI Flash_IS25LP064ALBLE	图纸描述	QSPI Flash子图
审核人员	*	审核日期	2019.10	产品名称	评估套件	产品型号	Easy-RT101x
				图纸版本	Rev.B	第 9 张 / 共 13 张	A4

10.Audio_MQS-SAI

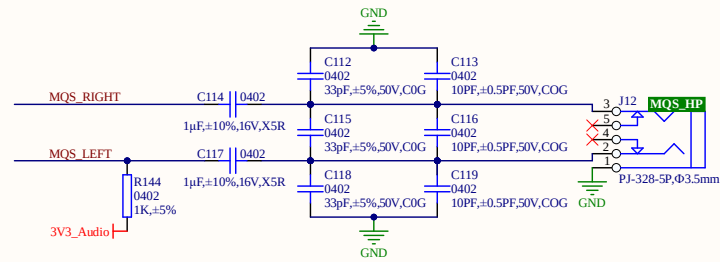
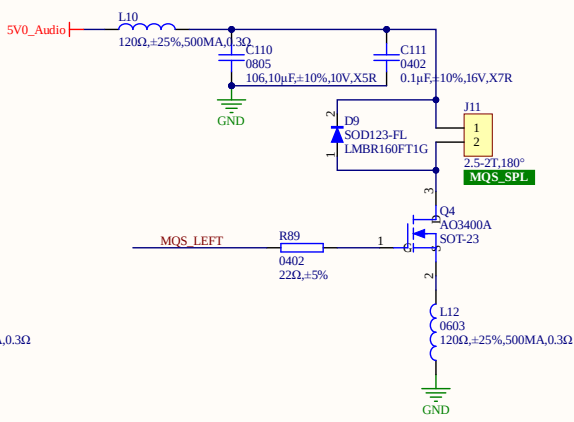
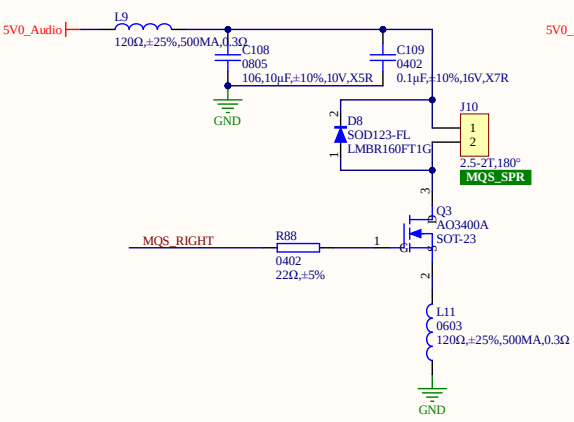
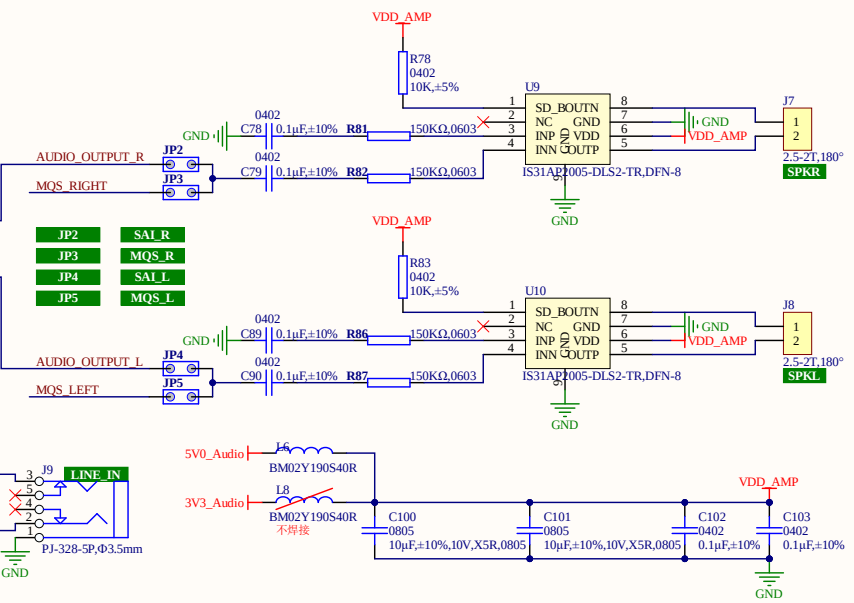
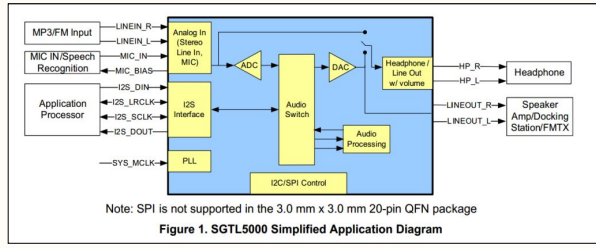
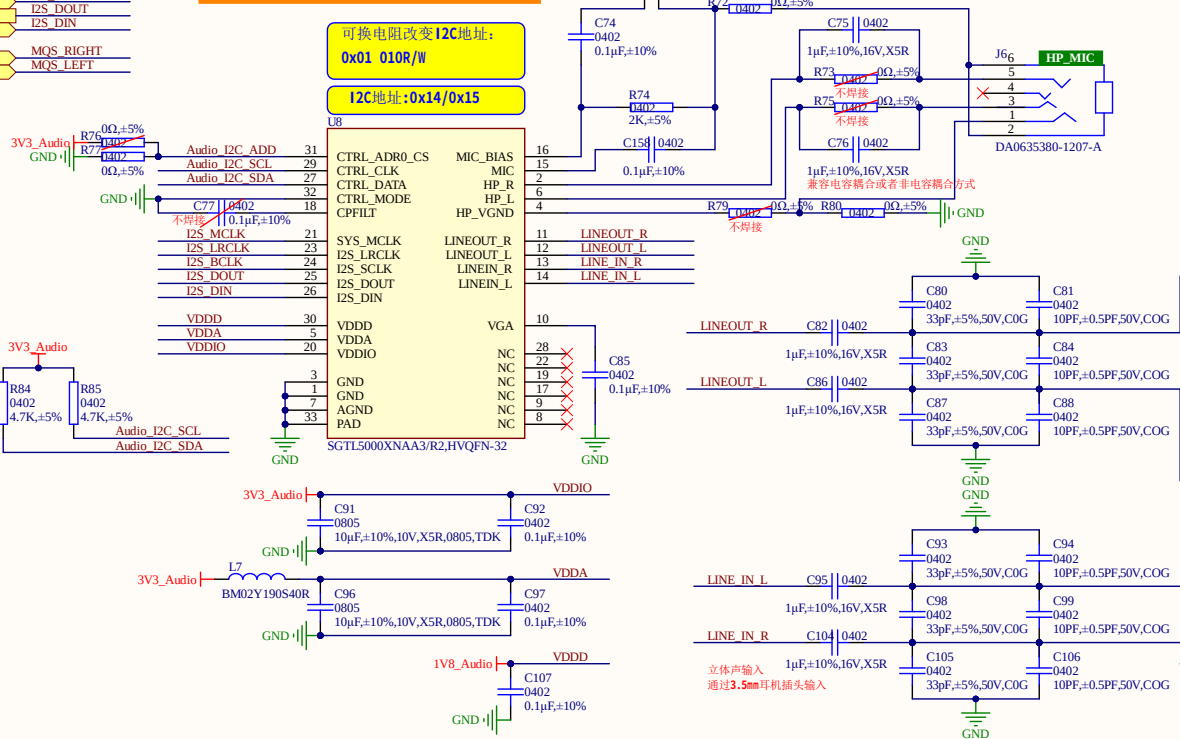


NOTE:
使用电容式MIC时,1脚需要接地,请将1脚接到开发板上其它排针上的GND上。建议客户画原理图时直接将1脚连接在GND上。

特别注意

可换电阻改变I2C地址:
0x01 010R/W

I2C地址:0x14/0x15



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11.WIFI-BT_AW-NM372SM

(11-1) WIFI/BT

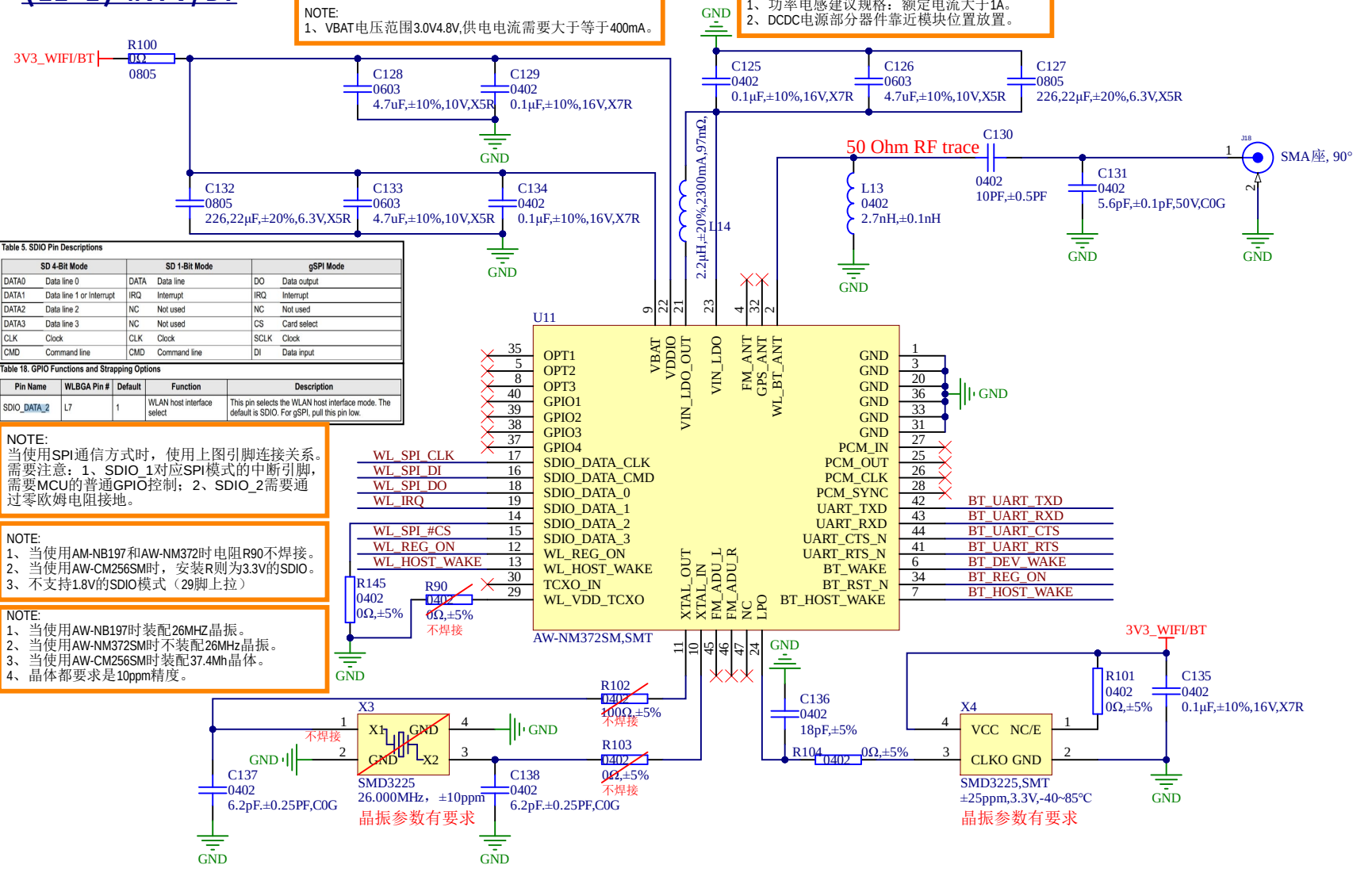
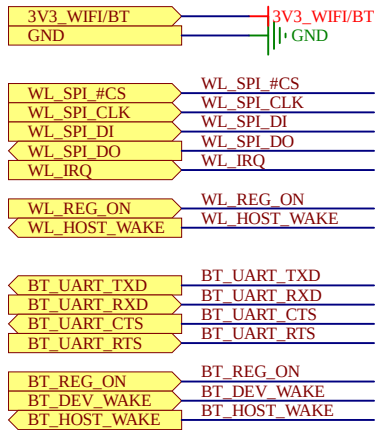


Table 5. SDIO Pin Descriptions

SD 4-Bit Mode		SD 1-Bit Mode		gSPI Mode	
DATA0	Data line 0	DATA	Data line	DO	Data output
DATA1	Data line 1 or Interrupt	IRQ	Interrupt	IRQ	Interrupt
DATA2	Data line 2	NC	Not used	NC	Not used
DATA3	Data line 3	NC	Not used	CS	Card select
CLK	Clock	CLK	Clock	CLK	Clock
CMD	Command line	CMD	Command line	DI	Data input

Table 18. GPIO Functions and Strapping Options

Pin Name	WLBGA Pin #	Default	Function	Description
SDIO_DATA2	L7	1	WLAN host interface select	This pin selects the WLAN host interface mode. The default is SDIO. For gSPI, pull this pin low.

NOTE:
当使用SPI通信方式时, 使用上图引脚连接关系。
需要注意: 1、SDIO_1对应SPI模式的中断引脚, 需要MCU的普通GPIO控制; 2、SDIO_2需要通过零欧姆电阻接地。

NOTE:
1、当使用AM-NB197和AW-NM372时电阻R90不焊接。
2、当使用AW-CM256SM时, 安装R则为3.3V的SDIO。
3、不支持1.8V的SDIO模式 (29脚上拉)

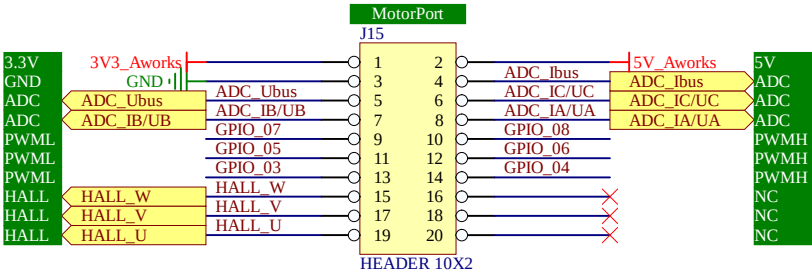
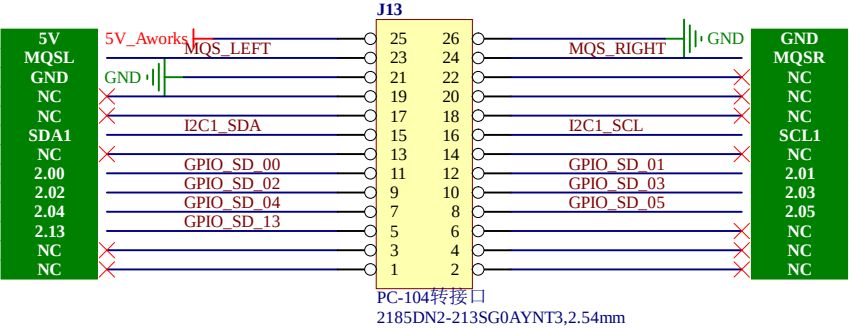
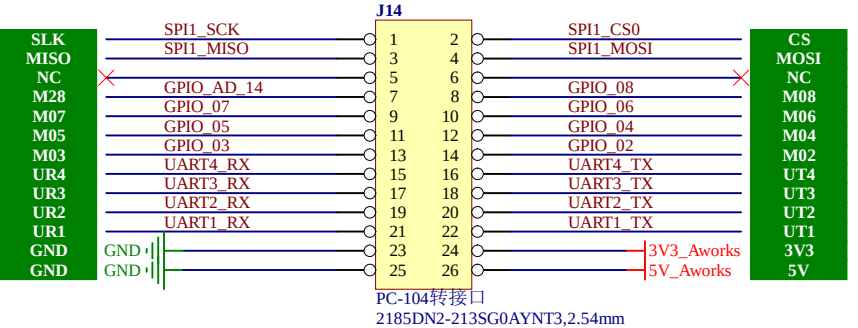
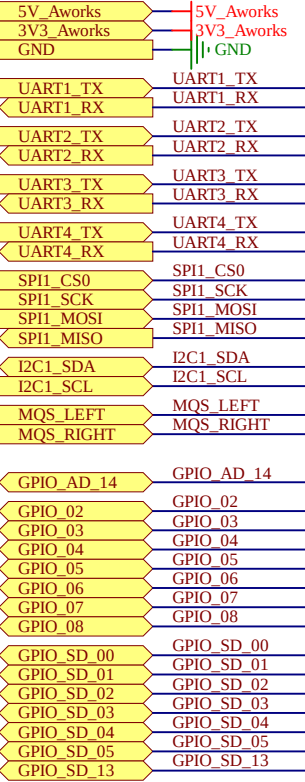
NOTE:
1、当使用AW-NB197时装配26MHz晶振。
2、当使用AW-NM372SM时不装配26MHz晶振。
3、当使用AW-CM256SM时装配37.4Mh晶体。
4、晶体都要求是10ppm精度。

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12.<图纸名称>

(12 1) Aworks-Port

NOTE:
1、注意RT系列主板与配板的引脚的兼容性。
2、IO口功能不局限于此，可通过找到对应IO口复用为其它功能。

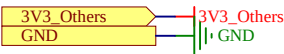


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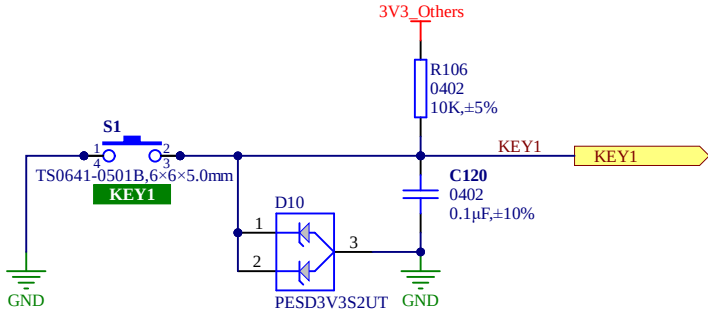
立功科技

设计人员	<设计人员>	设计日期	<设计日期>	图纸名称	<图纸名称>	图纸描述	<图纸描述>
审核人员	<审核日期>	审核日期	<审核人员>	产品名称	评估套件	产品型号	Easy-RT101x
						图纸版本	<图纸版本>第 12 张 / 共 13 张
							A4

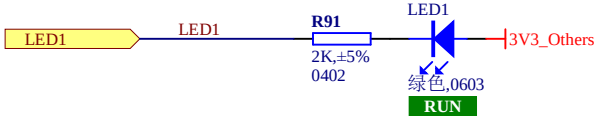
13.Others



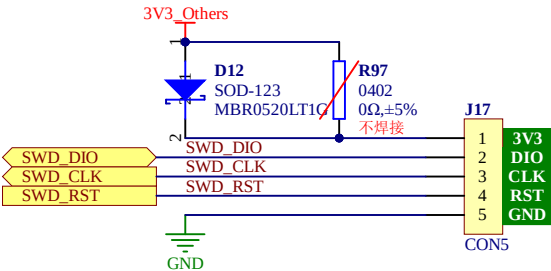
(13-1) KEY



(13-2) LED

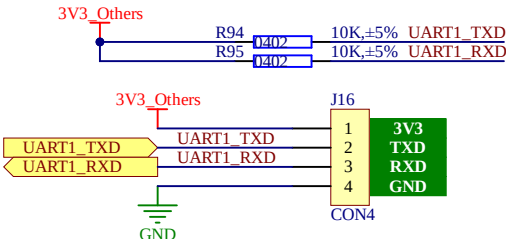


(13-3) SWD



NOTE:
1、为防止电流倒灌到RT的IO口，建议系统先上电再插入调试工具。
2、不建议通过调试工具为系统上电。

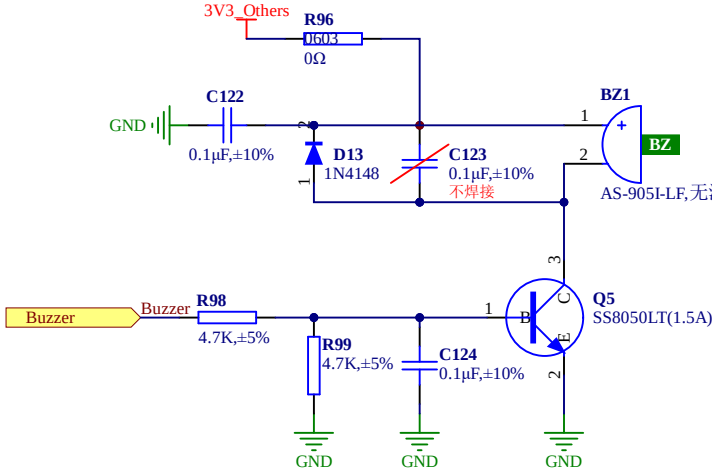
(13-4) UART1



NOTE:
1、调试串口UART1建议增加10K上拉电阻以防止误触发。
2、串行下载模式下，内部ROM先轮询UART1，如UART1没反应，则ROM将RT作为一个HID设备。

1.UART1	The Serial Downloader provides a means to download a program image to the chip over the USB and UART serial connections. In this mode, ROM programs WDOG1 for a time-out specified by the fuse WDOG Time-out Select (See the Fusemap chapter for details) if the WDOG_ENABLE eFuse is 1 and continuously polls for the USB and UART connection. If no activity is found on USB OTG1 and UART 1/2 and the watchdog timer expires, the Arm core is reset.	The ROM code firstly polls the UART1 signals from TXD1/RXD1. Add a 10-kΩ pull up resistor to the TXD1/RXD1 pins to avoid an invalid trigger of the UART port in the serial download mode.
2. USB1	If there is no polling activity from UART1 in the serial download mode, the ROM code acts as an HID device for PC downloading.	

(13-5) Buzzer



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